



Pixelworks Demonstrates Dual-Interface PW105 DisplayProcessor IC

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New Family of DisplayProcessor ICs with Integrated ADC and TMDS Receivers

Deliver All-in-one Solutions for XGA-Resolution LCD Monitors

Computex 2001, Taipei, June 4, 2001 — Pixelworks (Nasdaq:PXLW) demonstrated today the first member of its DisplayProcessor family to fully integrate both analog and digital input receivers with Pixelworks' award-winning performance and features. The PW105 DisplayProcessor IC targets the cost-sensitive XGA-resolution LCD monitor market with an all-in-one solution that speeds time to market while delivering outstanding image quality, broad compatibility, ease of use and lower overall system cost.

The PW105 DisplayProcessor IC enables fast-track development of mainstream XGA-resolution LCD monitors with an integrated dual-interface combining a triple 8-bit ADC with integrated PLLs and an embedded TMDS receiver that is fully compliant with the DVI 1.0 digital interface standard, including High-bandwidth Digital Content Protection (HDCP). Joining the PW105 in Pixelworks' new XGA-resolution monitor family will be the PW103 which will have a single analog input and PW101 which will have a single TMDS input. Designed to power the lowest cost XGA-resolution monitors, the PW105, PW103 and PW101 use a 160-pin PQFP package. Both the PW103 and PW101 are pin compatible with the PW105 providing customers maximum flexibility from a single circuit board design.

The new DisplayProcessor IC family includes Pixelworks' full suite of innovative features that make monitors powered by Pixelworks the benchmark for user-friendliness. They incorporate Pixelworks' advanced independent horizontal and vertical scaling to resize images ranging from VGA to SXGA for optimal display on XGA-resolution monitors. DisplayProcessor ICs include Pixelworks' patent-pending Fail-Safe™ mode designed to minimize user frustration while reducing support requirements and costs for monitor manufacturers. The Fail-Safe mode ensures users can always see their computer desktop even when the graphic signals driving the monitor are set at resolutions or refresh rates beyond the monitor's capabilities. They automatically detect and perfectly adjust incoming images with little or no user intervention, regardless of the input source.

"The PW105 is the culmination of our initiative to create a low-cost, dual-interface DisplayProcessor IC designed to drive XGA monitors into the mainstream by making them even more affordable while delivering Pixelworks' proven performance and ease of use into the highest volume segment for flat panel displays," said Charles Wang, Vice President and General Manager.

"The price-performance advantages inherent in the PW105 are a direct result of our ability to drive higher levels of integration by embedding sophisticated mixed signal design with our award winning technology," said Allen Alley, CEO, President, and Chairman of Pixelworks. "With the addition of the DisplayProcessor IC family, the Pixelworks product line spans the full spectrum of advanced display products from low-cost XGA monitors to full-featured advanced televisions and multimedia projectors."

The DisplayProcessor ICs will be priced below US\$15 in volume quantities with availability in the third quarter.

About Pixelworks, Inc.

Pixelworks, headquartered in Tualatin, Oregon, is the leading provider of system-on-a-chip ICs for the advanced display market. Pixelworks' solutions process and optimize video, computer graphics and Web information for display on a wide variety of devices used in business and consumer markets. For more information, please visit the company's Web site at www.pixelworks.com.